traveling between VHS 113 and set top box 90. For at least this reason, claims 1-3 and 5-10 are patentable over Lownes.

Dependent claim 2 is further patentable as Lownes nowhere discloses that the first housing is part of a modular platform adapted to receive replaceable cards.

Regarding claim 22, nowhere does Lownes disclose a housing including a plurality of slots each including a plug adapted to removably receive a card. For at least this reason, claims 22, 23 and 28 are patentable over Lownes.

## C. Rejection of Claims Under 35 U.S.C. § 103

Pending claims 4, 11, 20, 21 and 24 stand rejected under 35 U.S.C. § 103(a) over Lownes in view of U.S. Patent No. 5,699,426 (Tsukamoto). Claims 4 and 24 are patentable over the proposed combination at least for the reasons discussed above regarding claims 1 and 22, as neither reference teaches or suggests these claims. Claims 11, 20, and 21 are patentable as neither Lownes nor Tsukamoto teaches or suggests an encryption engine to provide two different levels of encryption.

Pending claims 13, 15-19, 25, 26 and 29 stand rejected under § 103(a) over Lownes in view of Tsukamoto and further view of U.S. Patent No. 5,784,427 (Bennett). This rejection is improper as there is no motivation to combine Lownes or Tsukamoto with Bennett, which relates to feedback and shift units, and not digital television systems.

Pending claims 14, 27 and 30 stand rejected under § 103(a) over Lownes in view of Tsukamoto in view of Bennett in further view of U.S. Patent No. 5,969,909 (Warren). The rejection of the above claims is improper, as there is no motivation or suggestion to combine these four references. Because there is no motivation to combine the references in order to obtain the

claimed subject matter, a *prima facie* case of obviousness has not been made, and claims 14, 27 and 30 are patentable.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. The Commissioner is authorized to charge any additional fees or credit any overpayment to Deposit Account No. 20-1504.

Respectfully submitted,

Date:	July	7.	2003
Date.	oury	′,	2003

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## APPENDIX

**Technology Center 2100** 

- 1 1. (Amended) A digital television system comprising:
- 2 a first and second housing;
- a receiver, adapted to receive a digital television signal,
- 4 in said first housing;
- 5 a digital television display in said second housing; and
- a digital graphics bus [coupling] coupled to said receiver
- 7 in said first housing and said display in said second housing.
- 1 11. (Amended) A digital graphics bus [for coupling] to
- 2 couple a digital television receiver and a digital television
- 3 display comprising:
- a encryption engine coupled to said bus [for encrypting] to
- 5 encrypt signals transferred from said receiver to said bus, said
- 6 encryption engine to provide two different levels of encryption;
- 7 and
- a decryption engine coupled to said bus [for decrypting] to
- 9 <u>decrypt</u> signals transferred from said bus to said display.
- 1 13. (Amended) The bus of claim [12] 11 wherein said bus is
- 2 adapted to periodically encrypt [encrypts] at a higher level of
- 3 encryption.
- 1 14. (Amended) The bus of claim 13 wherein the level of
- 2 encryption is adapted to change [changed] on frame boundaries.
- 1 23. (Amended) The platform of claim 22 wherein one of said
- 2 slots is coupled to receive [receives] a motherboard with a
- 3 processor.

- 1 26. (Amended) The platform of claim [22] 25 wherein said
- 2 encryption levels are changed periodically.